

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

SUNDEEP M. BAIKAR, ET AL.

Application No.: 10/643,678

Filed: August 18, 2003

For: **Low Pin Count Docking Architecture for a
Trusted Platform**

Art Group: 2139

Examiner: Kent Williams

Confirmation No.: 4611

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

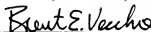
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The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: June 26, 2007



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I hereby certify that this correspondence is being submitted electronically via EFS Web on the date shown below.



Wendi Lou Rostan

06-26-07

Date

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	Application Number	10/643,678
	Filing Date	August 18, 2003
	First Named Inventor	Bajikar, Sundeeep
	Group Art Unit	2139
	Examiner Name	Williams, Kent
Sheet 1 of 2		Attorney Docket No: 42P16632

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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		FABRY, R.S., "Capability-Based Addressing", <u>Fabry, R.S., "Capability-Based Addressing," Communications of the ACM, Vol. 17, No. 7, (July 1974),403-412</u>	
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		INTEL CORPORATION, "IA-32 Intel Architecture Software Developer's Manual", <u>Volume 3: System Programming Guide, Intel Corporation - 2003,13-1 through 13-24</u>	
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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		INTEL CORPORATION, "Intel386 DX Microprocessor 32-Bit CHMOS Microprocessor With Integrated Memory Management", (1995),5-56	
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